

INFORMATION DISCLOSURE STATEMENT

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William P. Moore, et al.

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,046,040	09/91	Akio Miyoshi			
	5,101,344	03/92	Bonet, et al.			
	4,887,203	12/89	MacGregor, et al.			
	4,472,772	09/84	Laurence P. Flora			
	4,484,268	11/84	Thoma, et al.			
	4,713,750	12/87	Damouny, et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Computer Organization and Design- The Hardware/Software Interface; John L. Hennessy and David A. Patterson; Morgan Kaufman Publishers; Second Edition; 1998; Pages B-11 to B-13.

Computer Architecture-A Quantitative Approach; John L. Hennessy and David A. Patterson; Morgan Kaufman Publishers; Fourth Printing; 1990; Pages 205-206.

EXAMINER

DATE CONSIDERED

5-October-2004

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.